

# SQA11N90C

## 900V 11A N-channel Enhancement Mode Power MOSFET

### Features

- Typical on-Resistance:  $R_{DS(on)}=0.95\ \Omega$
- High Blocking Voltage
- 100% Avalanche Test
- Good Stability and Uniformity with High  $E_{AS}$

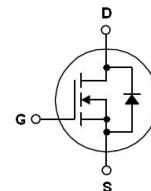
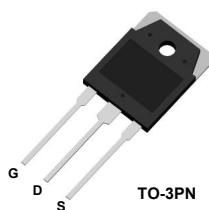
### Description

The SQA11N90C is an high blocking voltage N-Channel power MOSFET. This device provide excellent performance for high voltage power supplies or pulse circuits.

### Applications

- High Voltage Power Supplies
- Capacitor Discharge Applications
- Pulse Circuits

### Package Type & Internal Circuit



### Absolute Maximum Ratings @ $T_c=25\text{ }^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Ratings	Unit
$V_{DSS}$	Drain to Source Voltage	900	V
$V_{GSS}$	Gate to Source Voltage	$\pm 30$	V
$I_D$	Drain Current	$T_c=25\text{ }^\circ\text{C}$	11
		$T_c=100\text{ }^\circ\text{C}$	7
$I_{DM}$	Pulsed Drain Current (Note1)	50	A
$P_D$	Maximum Power Dissipation $T_c=25\text{ }^\circ\text{C}$	340	W
	Derate above $25\text{ }^\circ\text{C}$	2.4	$\text{W}/\text{ }^\circ\text{C}$
$E_{AS}$	Single Pulsed Avalanche Energy (Note 2)	900	mJ
$T_J$	Operating Junction Temperature Range	-50~+150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-50~+150	$^\circ\text{C}$

### Thermal Characteristics

Symbol	Parameter	Ratings	Unit
$R_{th(J-C)}$	Thermal Resistance, Junction to case	0.36	$^\circ\text{C}/\text{W}$
$R_{th(J-A)}$	Thermal Resistance, Junction to Ambient	32	$^\circ\text{C}/\text{W}$

**Electrical Characteristics** @ $T_C=25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain to Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	900	-	-	V
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	3.0	4.0	5.0	V
$R_{DS(\text{on})}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}, I_D=5.5\text{A}$	-	0.95	1.2	$\Omega$
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=V_{DSS}, V_{GS}=0\text{V}$	-	-	10	$\mu\text{A}$
$I_{GSS}$	Gate to Source Leakage Current	$V_{GS}=V_{GSS}, V_{DS}=0\text{V}$	-	-	$\pm 100$	nA

**D-S Diode Characteristics and Maximum Rating** @ $T_C=25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_S$	Maximum Drain to Source Diode Forward Current		-	-	11.0	A
$V_{SD}$	Drain-Source Diode Forward Voltage	$V_{GS}=0\text{V}, I_S=11\text{A}$	-	0.9	1.4	V
$t_{rr}$	Reverse Recovery Time	$V_{GS}=0\text{V}, I_S=11\text{A}, \frac{dI}{dt}=-100\text{A}/\mu\text{s}$	-	850	-	ns
$Q_{rr}$	Reverse Recovery Charge		-	20	-	$\mu\text{C}$

**Switching Characteristics** @ $T_C=25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on Delay Time	$I_D=11\text{A}, V_{DD}=450\text{V}, R_G=25\Omega$ (Note 3)	-	50	140	ns
$t_r$	Turn-on Rise Time		-	120	280	ns
$t_{d(off)}$	Turn-off Delay Time		-	140	260	ns
$t_f$	Turn-off Fall Time		-	90	190	ns
$C_{iss}$	Input Capacitance	$V_{GS}=0\text{V}, V_{DS}=25\text{V}, f=1.0\text{MHz}$	-	2750	3300	pF
$C_{oss}$	Output Capacitance		-	270	310	pF
$C_{rss}$	Reverse Transfer Capacitance		-	25	32	pF
$Q_g$	Total Gate Charge	$I_D=11\text{A}, V_{DD}=720\text{V}, V_{GS}=10\text{V}$ (Note 3)	-	58	90	nC
$Q_{ge}$	Gate to Emitter Charge		-	14	-	nC
$Q_{gc}$	Gate to Collector Charge		-	26	-	nC

**Note:**

1. Repetitive rating: pulse-width limited by maximum junction temperature
2.  $V_{DD}=50\text{V}, L=15\text{mH}, V_{\text{clamp}}=1100\text{V}, V_G=10\text{V}, I_D=11\text{A}$
3. Essentially independent of operating temperature typical characteristics

## Typical Performance Characteristics

Fig. 1. Typical on-Resistance Characteristics

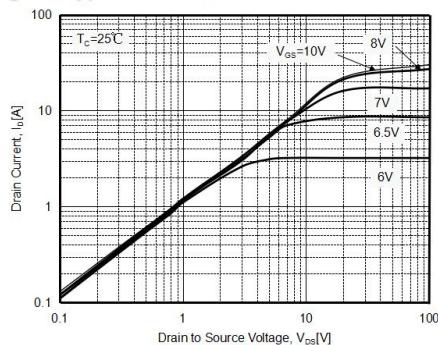


Fig. 2. Typical Transfer Characteristics

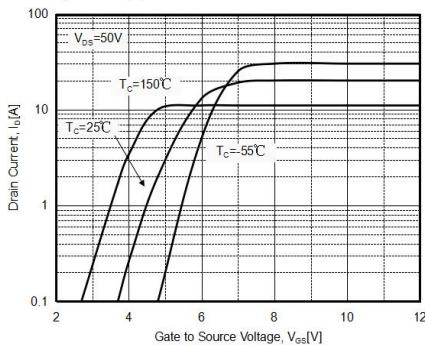


Fig. 3. Static on-Resistance vs.  $I_D$

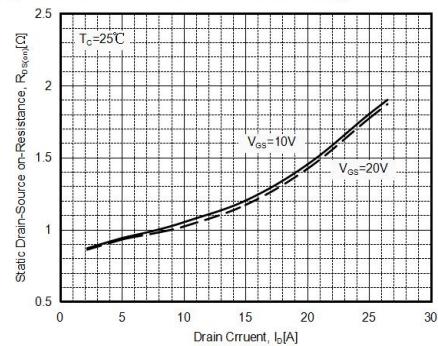


Fig. 4. Body Diode Forward Voltage vs.  $I_{DR}$

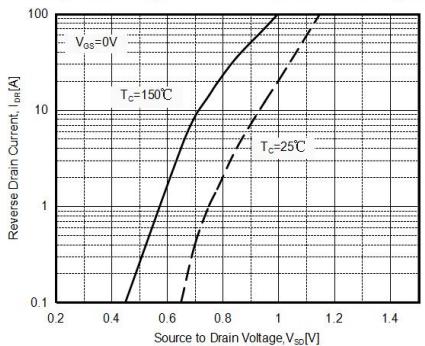


Fig. 5. Capacitance Characteristics

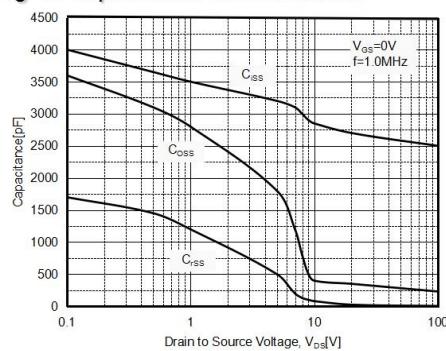
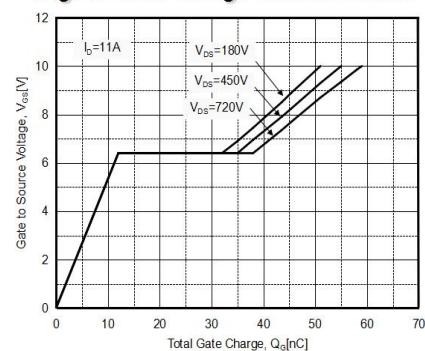


Fig. 6. Gate Charge Characteristics



## Typical Performance Characteristics

Fig. 7. Breakdown Voltage vs. Temperature

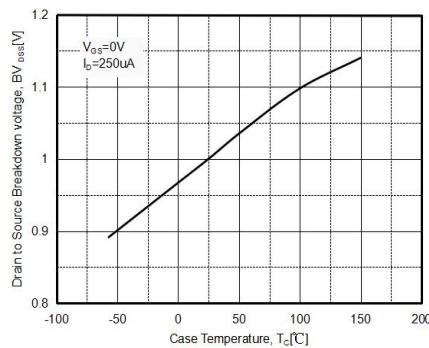


Fig. 8. Static on-Resistance vs. Temperature

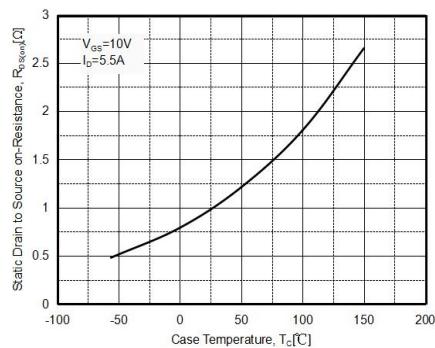


Fig. 9. Maximum Safe Operating Area

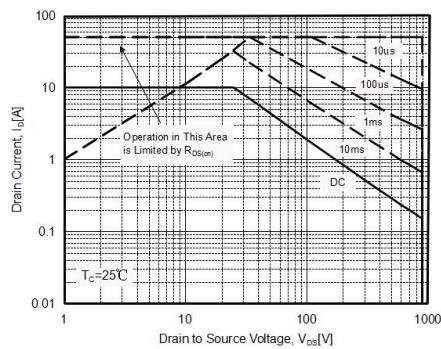
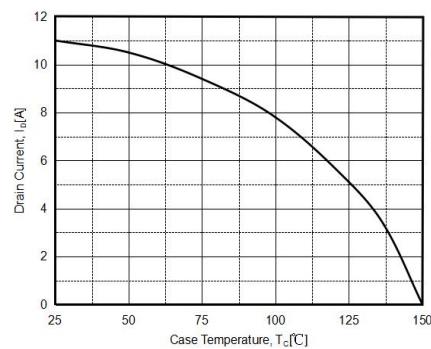


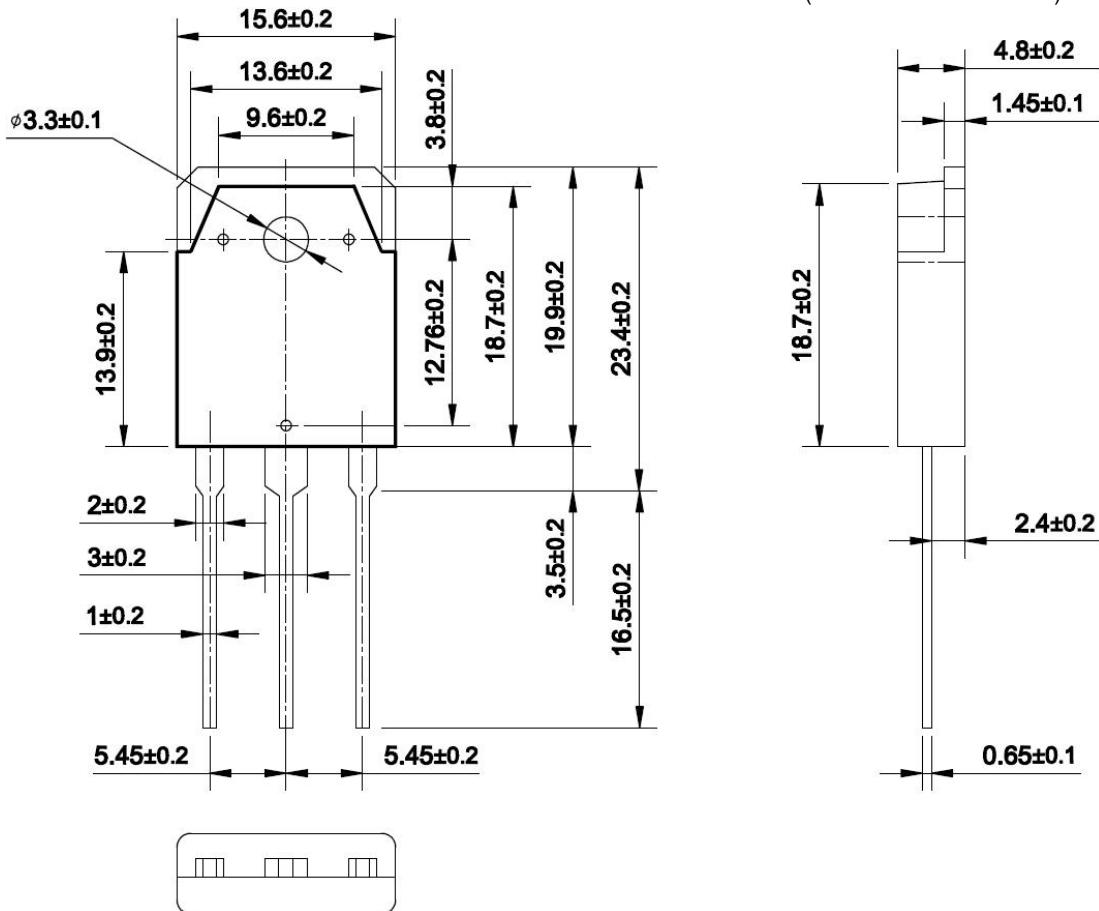
Fig. 10. Maximum Drain Current vs. Temperature



## Package Dimensions

**TO-3PN**

(Dimensions in Millimeters)



### DISCLAIMER:

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