

SCQH070N120

1200V 70mΩ N-Channel SiC Power MOSFET

Features

- Typical on-Resistance: $R_{DS(on)}=70\text{m}\Omega(\text{typ.})$
- High Blocking Voltage
- 100% Avalanche Test
- Good Stability and Uniformity with High E_{AS}

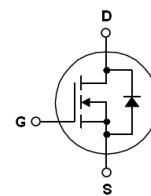
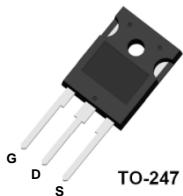
Description

The SCQH070N120 is a high blocking voltage N-Channel SiC power MOSFET. This device provide excellent performance for high voltage power supplies or pulse circuits.

Applications

- Solar Inverters
- High Voltage DC/DC Converters
- Motor Drivers
- Switch Mode Power Supplies

Package Type & Internal Circuit



Absolute Maximum Ratings @ $T_C=25^\circ\text{C}$ unless otherwise noted

| Symbol | Parameter | Ratings | Unit |
|------------|--|-------------------------|---------------------|
| V_{DSS} | Drain to Source Voltage | 1200 | V |
| V_{GSS} | Gate to Source Voltage | -10/+25 | V |
| V_{GSop} | Recommended operation Values of Gate -Source Voltage | -5/+20 | V |
| I_D | Drain Current | $T_C=25^\circ\text{C}$ | A |
| | | $T_C=100^\circ\text{C}$ | A |
| I_{DM} | Pulsed Drain Current (Note1) | 144 | A |
| P_D | Maximum Power Dissipation | $T_C=25^\circ\text{C}$ | W |
| | Derate above 25°C | | W/ $^\circ\text{C}$ |
| E_{AS} | Single Pulsed Avalanche Energy (Note 2) | 180 | mJ |
| T_J | Operating Junction Temperature Range | -50~+175 | $^\circ\text{C}$ |
| T_{STG} | Storage Temperature Range | -50~+175 | $^\circ\text{C}$ |

Thermal Characteristics

| Symbol | Parameter | Ratings | Unit |
|---------------|---|---------|---------------------------|
| $R_{th(J-C)}$ | Thermal Resistance, Junction to case | 0.62 | $^\circ\text{C}/\text{W}$ |
| $R_{th(J-A)}$ | Thermal Resistance, Junction to Ambient | 40 | $^\circ\text{C}/\text{W}$ |

Electrical Characteristics @ $T_C=25\text{ }^\circ\text{C}$ unless otherwise noted

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|--------------|-----------------------------------|-------------------------------------|------|------|-----------|------------------|
| BV_{DSS} | Drain to Source Breakdown Voltage | $V_{GS}=0\text{V}, I_D=2\text{mA}$ | 1200 | - | - | V |
| $V_{GS(th)}$ | Gate Threshold Voltage | $V_{DS}=V_{GS}, I_D=5\text{mA}$ | 1.8 | 2.3 | 4.0 | V |
| $R_{DS(on)}$ | Static Drain-Source On-Resistance | $V_{GS}=20\text{V}, I_D=20\text{A}$ | - | 70 | 86 | $\text{m}\Omega$ |
| I_{DSS} | Zero Gate Voltage Drain Current | $V_{DS}=V_{DSS}, V_{GS}=0\text{V}$ | - | - | 100 | μA |
| I_{GSS} | Gate to Source Leakage Current | $V_{GS}=V_{GSS}, V_{DS}=0\text{V}$ | - | - | ± 500 | nA |

D-S Diode Characteristics and Maximum Rating @ $T_C=25\text{ }^\circ\text{C}$ unless otherwise noted

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|----------|------------------------------------|---|------|------|------|------|
| V_{SD} | Drain-Source Diode Forward Voltage | $V_{GS}=-5\text{V}, I_S=10\text{A}$ | - | 3.6 | - | V |
| t_{rr} | Reverse Recovery Time | $V_{GS}=0\text{V}, I_S=20\text{A},$ $dI/dt=-290\text{A}/\mu\text{s}$ | - | 37 | - | ns |
| Q_{rr} | Reverse Recovery Charge | | - | 94 | - | nC |

Switching Characteristics @ $T_C=25\text{ }^\circ\text{C}$ unless otherwise noted

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|--------------|------------------------------|--|------|------|------|------|
| $t_{d(on)}$ | Turn-on Delay Time | $I_D=20\text{A},$ $V_{DD}=800\text{V},$ $R_G=2.5\Omega$ $V_{GS} = -5/20\text{V},$ (Note 3) | - | 11 | - | ns |
| t_r | Turn-on Rise Time | | - | 9 | - | ns |
| $t_{d(off)}$ | Turn-off Delay Time | | - | 18 | - | ns |
| t_f | Turn-off Fall Time | | - | 8 | - | ns |
| C_{iss} | Input Capacitance | $V_{GS}=0\text{V}, V_{DS}=1000\text{V},$ $f=1.0\text{MHz}$ | - | 1695 | - | pF |
| C_{oss} | Output Capacitance | | - | 107 | - | pF |
| C_{rss} | Reverse Transfer Capacitance | | - | 7 | - | pF |
| Q_g | Total Gate Charge | $I_D=20\text{A},$ $V_{DD}=800\text{V}$ $V_{GS}=-5/20\text{V}$ (Note 3) | - | 92 | - | nC |
| Q_{gs} | Gate to Source Charge | | - | 28 | - | nC |
| Q_{gd} | Gate to Drain Charge | | - | 17 | - | nC |

Note:

1. Repetitive rating: pulse-width limited by maximum junction temperature
2. $V_{DD}=100\text{V}, L=1\text{mH}, V_{clamp}=1600\text{V}, V_G=10\text{V}, I_D=19.0\text{A}$
3. Essentially independent of operating temperature typical characteristics

Typical Performance Characteristics

Fig. 1. Typical on-Resistance Characteristics

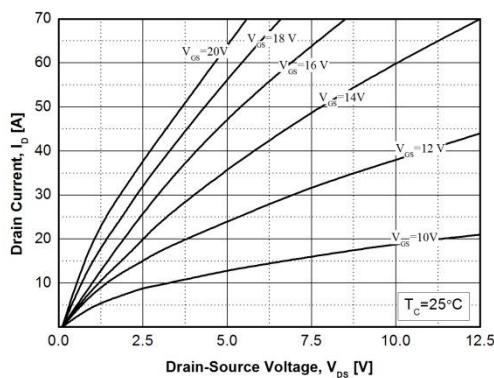


Fig. 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

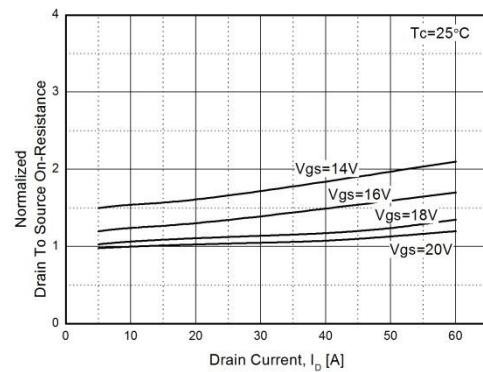


Fig. 3. Normalized On-Resistance vs. Junction Temperature

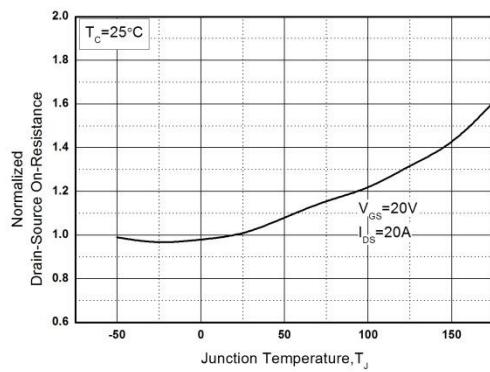


Fig. 4. On-Resistance vs. Gate-to-source Voltage

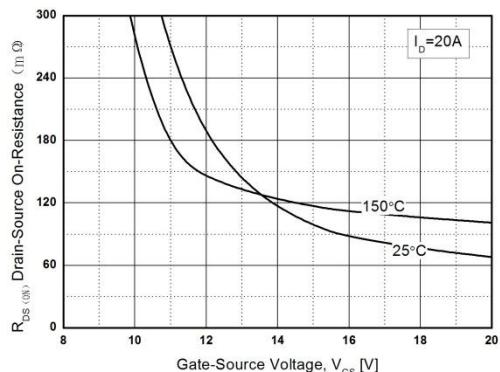


Fig. 5. Transfer Characteristics

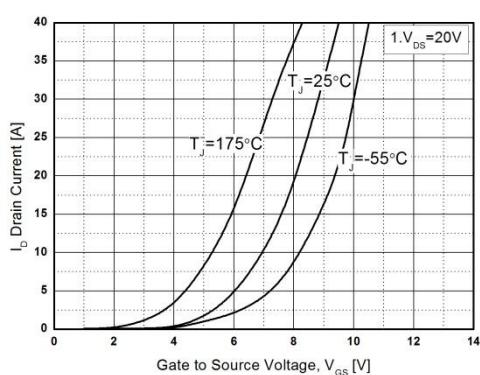
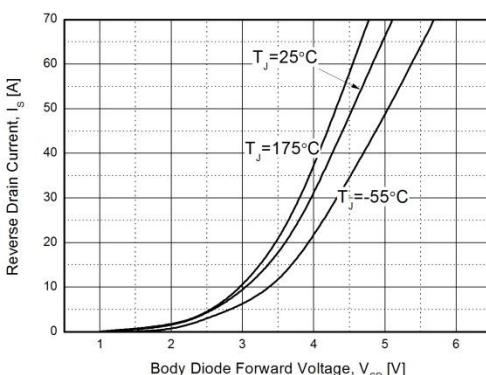


Fig. 6. Source-to-Drain Diode Forward Voltage vs. Source Current



Typical Performance Characteristics

Fig. 7. Gate Charge Characteristics

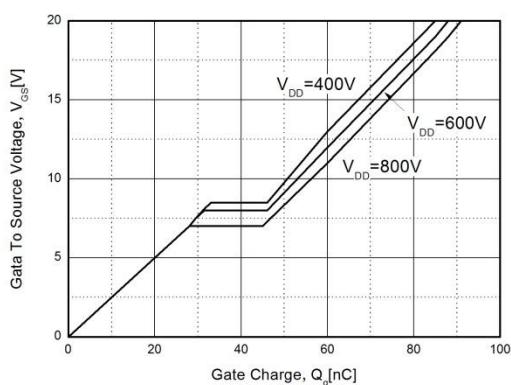


Fig. 8. Characteristics vs. Drain-to-Source Voltage

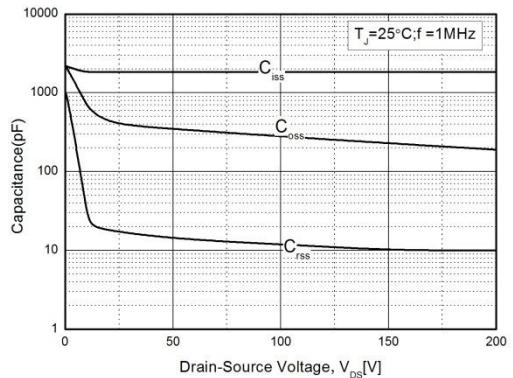
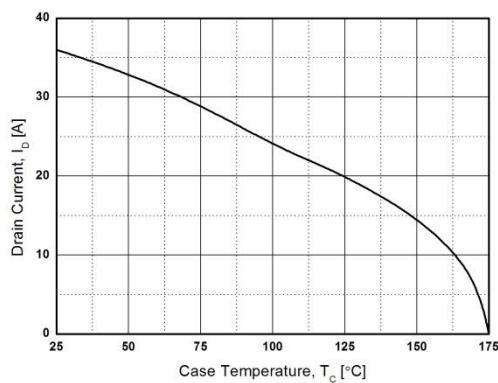


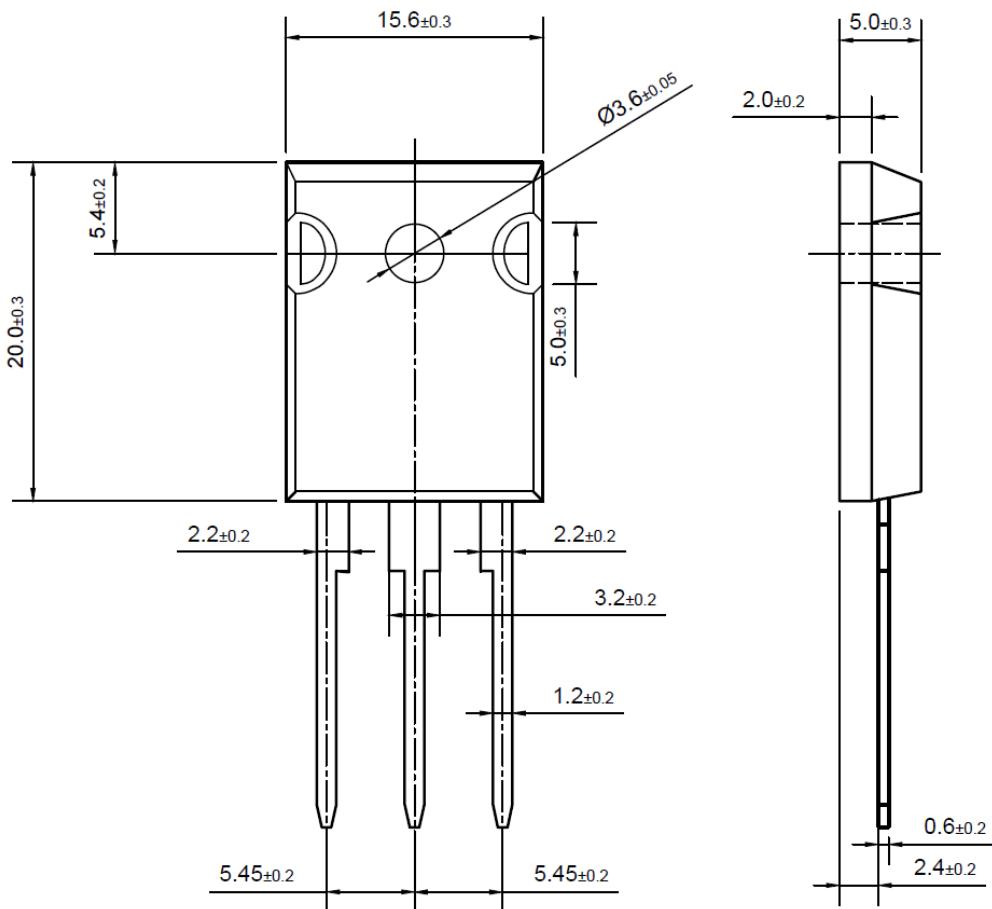
Fig. 9. Maximum Drain Current vs. Temperature



Package Dimensions

TO-247

(Dimensions in Millimeters)



DISCLAIMER:

The products are not designed for use in hostile environments, including, without limitation, aircraft, nuclear power generation, medical appliances, and devices or systems in which malfunction of any product can reasonably be expected to result in a personal injury. Seller's customers using or selling seller's products for use in such applications do so at their own risk and agree to fully defend and indemnify Seller.

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